

# RUIXIANG JIANG BACHELOR

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**EDUCATION**      **Department of Engineering, Southern University of Science and Technology**    Shen-zhen, China  
*B.E. in Computer Science and Engineering*      2021 - 2025  
    • GPA: 3.73/4.00

**PUBLICATIONS**    1. **Ruixiang Jiang**, Yufan Jia, Juntao You, Yinqian Zhang. Patent on Asterinas IMA (Ap-plying), 2024.

**PROJECTS**

- Supported Firecracker virtualization for Asterinas**  
*Teecert Lab*      2023.09 - 2024.01
- Implemented Integrity Measurement Architecture for Asterinas TDX version**  
*Teecert Lab*      2024.02 - 2024.06
- Built benchmarks for UNSAFE-to-FFM automatic transformation**  
*Prof. Joshua's Lab*      2024.07 - 2024.09
- Implementing Hardware Security Module for Asterinas**  
*Teecert Lab*      2024.09 - 2024.11
- A new kind of indirect prompt injection for Large Language Model**  
*Teecert Lab*      2024.11 - current

**AWARDS AND HONORS**

- **First Prize**, SUSTech Freshman Scholarship      2021
- **Second Prize**, SUSTech Outstanding Student Scholarship      2022
- **Top Ten Volunteers Awards**, SUSTech      2022
- **Second Prize**, SUSTech Outstanding Student Scholarship      2023
- **Outstanding Student Assistant**, Engineering College, SUSTech      2024
- **Team Champion**, Engineering College Badminton Competition, SUSTech      2024

**SKILLS**

**Languages:** Chinese, English.  
**Programming:** Python, C/C++, Rust, Java.  
**Interests:** Cybersecurity, Operating Systems, Software Engineering